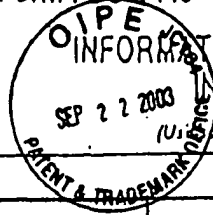


Form PTO-1449

INFORMATION DISCLOSURE CITATION
AN APPLICATION

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Docket Number (Optional)

HALO-02-004

Application Number

10/602,228

Applicant

Ki-Tae Park et al.

Filing Date

06/24/03

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2828

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	TITLE	CLASS	SUBCLASS	ALSO DATE IF APPROPRIATE
11	6418040	7/9/02	Meng	363	60	4/9/01
	6212107	4/3/01	Tsukada	365	189.09	12/14/99
	6130574	10/10/00	Bloch et al.	327	536	7/26/99
	6046625	4/4/00	Menichelli	327	536	12/18/97
	5925905	7/20/99	Hanneberg et al.	257	299	7/24/97
	5815026	9/29/98	Santin et al.	327	536	7/19/96
	5986947	11/16/99	Choi et al.	365	189.11	4/9/98
✓	6100557	8/8/00	Hung et al.	257	299	10/10/96

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Portmox Pages, Etc.)

11	"A S-V-Only 0.6μm Flash EEPROM with Row Decoder Scheme in Triple-Well Structure", IEEE Journal of Solid State Circuits, Vol. 27, No. 11, Nov. 1992, pp. 1540-1545.
11	"On-Chip High-Voltage Generation in MNOS Integrated Circuits Using Improved Voltage Multiplier Technique, IEEE Journal of Solid-State Circuits, Vol. 11, No. 3, June 1976, pp. 374-378.

EXAMINER

Sterrett

DATE CONSIDERED

12/16/04

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